



# Morsels: Explicit Virtual Memory Objects DIMES '23

Alexander Halbuer

Christian Dietrich Florian Rommel



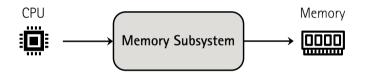
Leibniz Universität Hannover halbuer@sra.uni-hannover.de

October 23, 2023

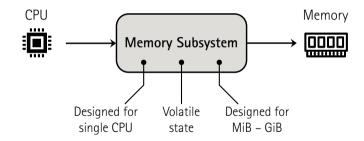


Daniel Lohmann

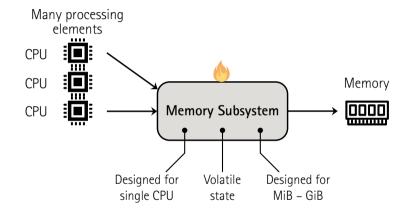




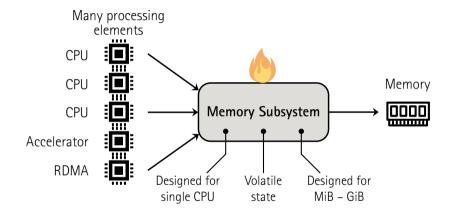




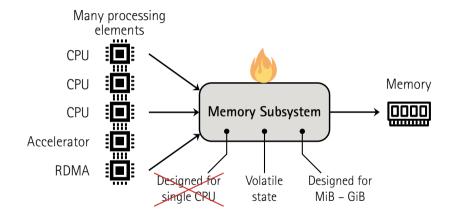




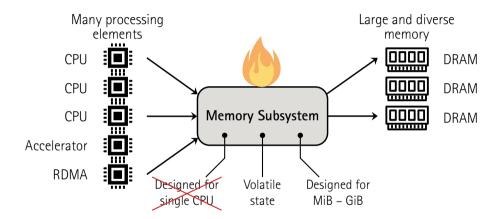




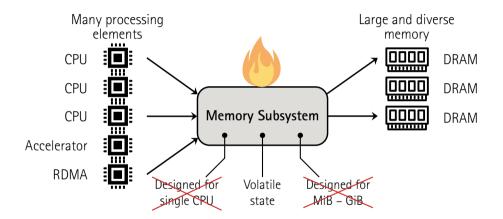




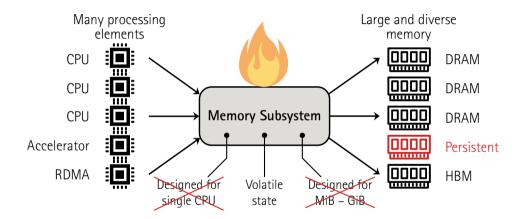




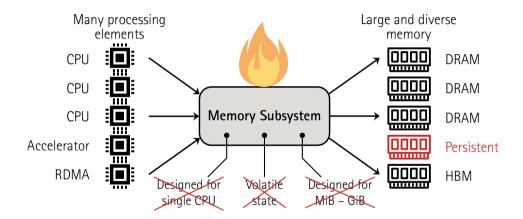




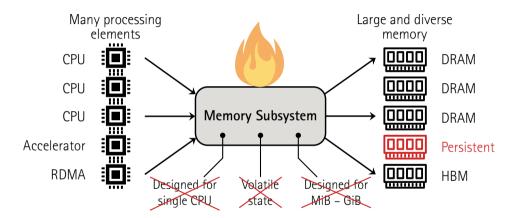






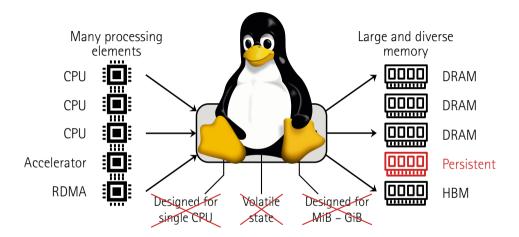






#### What are efficient OS abstractions for large memory objects?





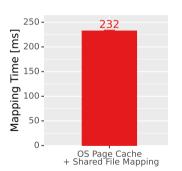
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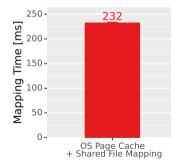
# Mapping $\sim$ 7 GiB of data into an application's address space





- Conventional file mapping (pre-faulted)
  - High setup costs
  - Create new page tables
  - Populate with pages from page cache

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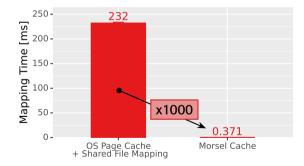




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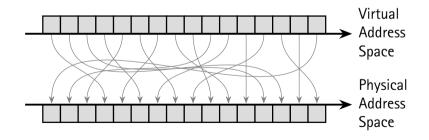
#### Our solution: Morsels

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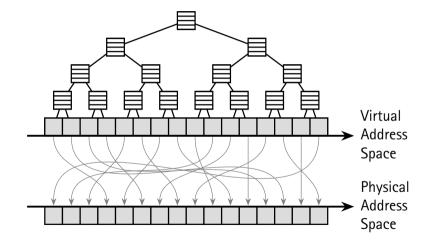






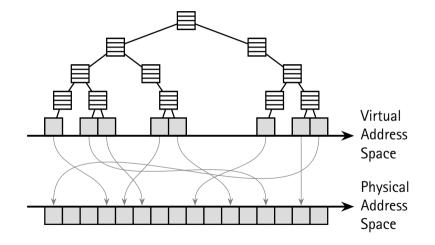






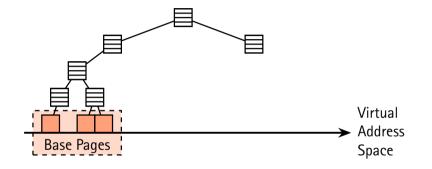






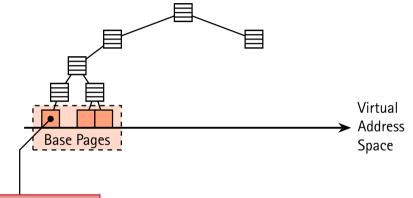








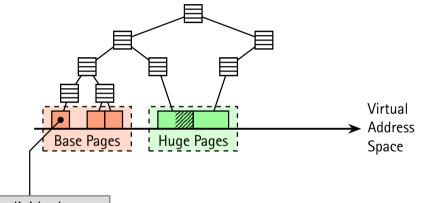




## Many individual pages



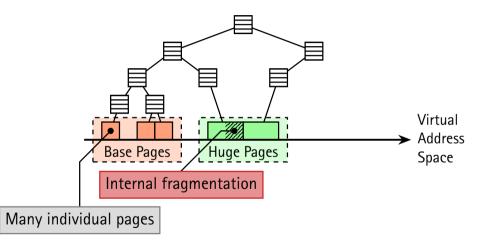




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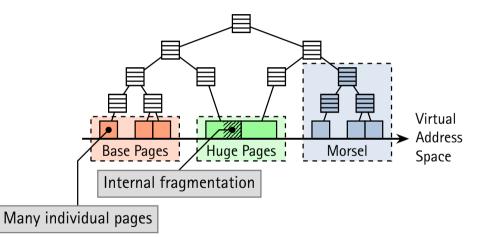






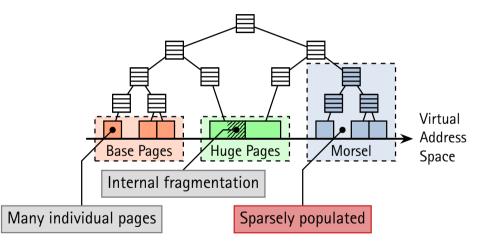






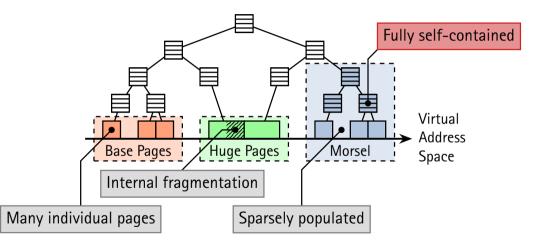






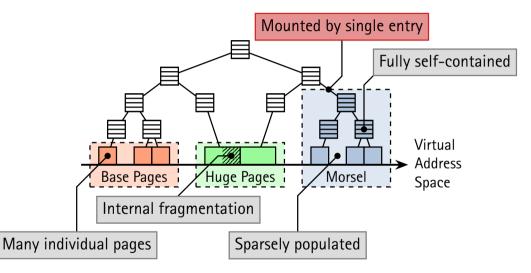






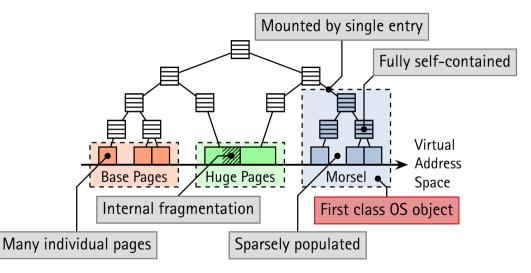
## SRA Memory Objects - Morsel Concept





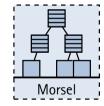
## RA Memory Objects - Morsel Concept







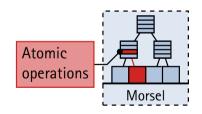






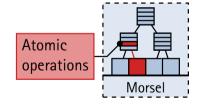


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  - No locking required



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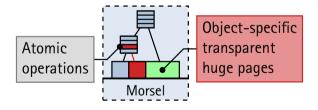
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  - Fully self-contained design
  - Crash consistent implementation





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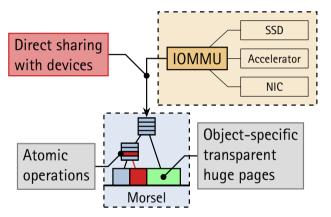
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- Huge-page support
  - Increase TLB coverage
  - Transparency on object granularity
- Sharing with devices
  - Reduce management overhead for DMA





## SRA Case Study 1: User-Space Read-Only File Cache



**Evaluation:** Prototypic implementation in Linux

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  - Uses individual 4-KiB pages
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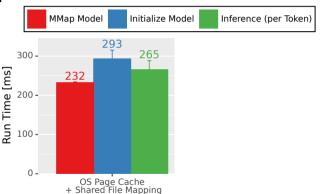


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llama.cpp with SelFee model: 6.82 GiB



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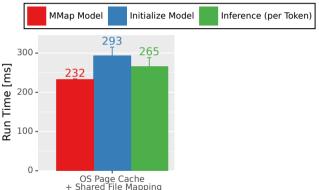
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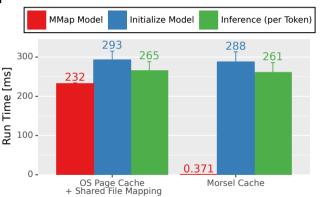
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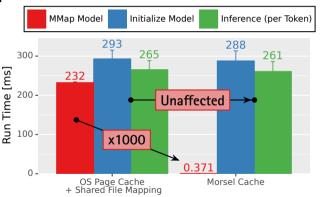


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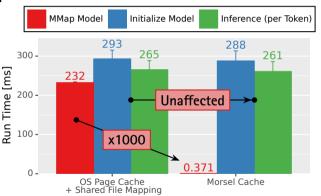
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### Result: 45% reduced startup time

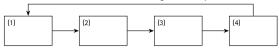
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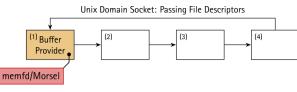




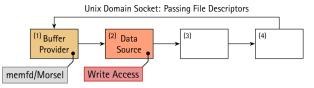




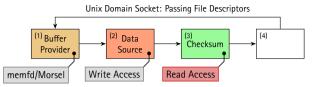




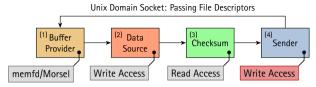




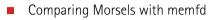




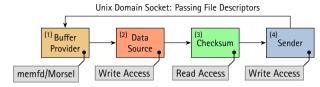








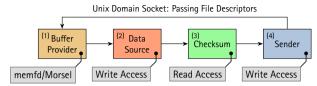
- 1000 packets per type
- Up to 256 MiB packet size

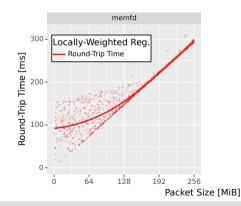




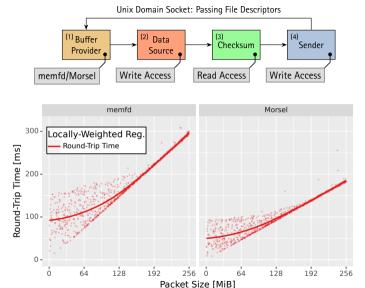


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# Comparing Morsels with memfd

300-

100

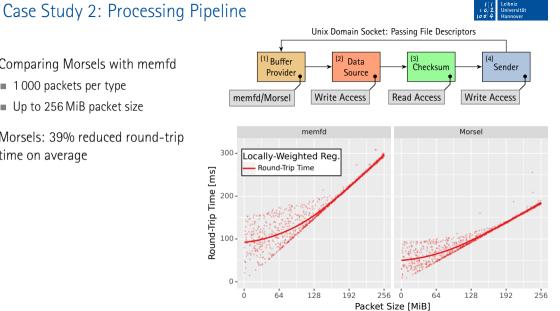
0 -

Round-Trip Time [ms] 200-

1000 packets per type

SRA

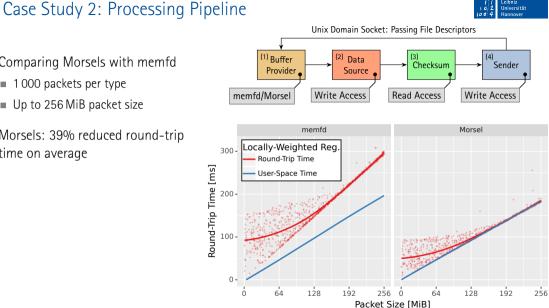
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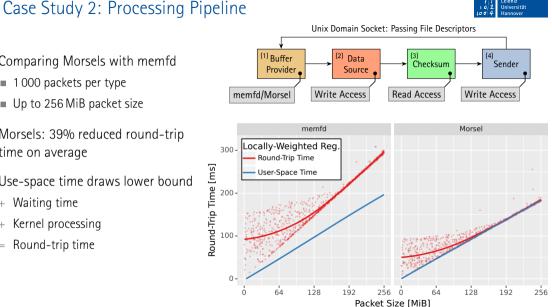
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  - Waiting time +

SRA

- Kernel processing +
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Round-Trip Time [ms]





# Comparing Morsels with memfd

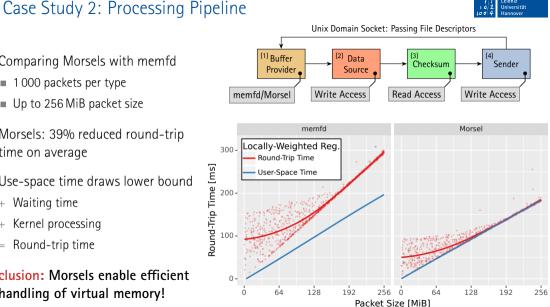
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### Conclusion: Morsels enable efficient handling of virtual memory!

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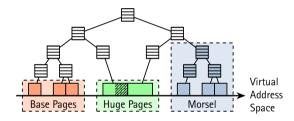




# SRA Morsels: Highly efficient handling of large memory objects



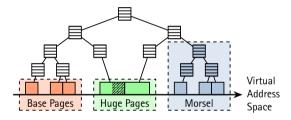
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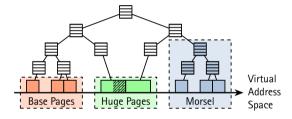
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  - Results
    - Constant mapping time (x1000 faster for 7 GiB)
    - 39% reduced RTT for shown pipeline







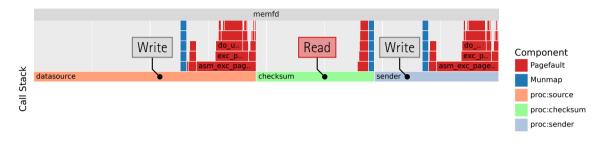
#### Run-Time (Normalized to memfd) [%]





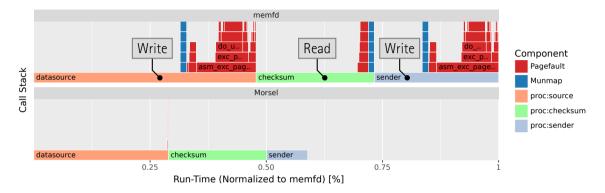
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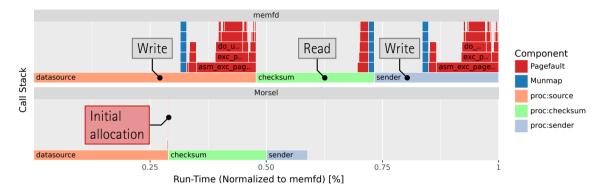


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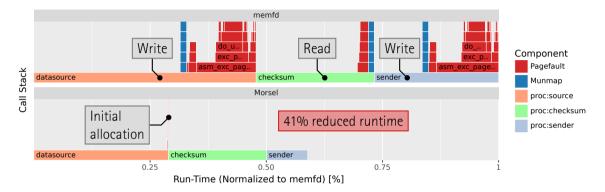




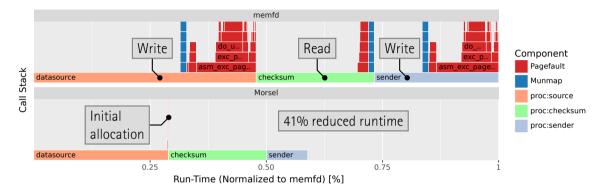












### Conclusion: Morsels enable efficient handling of virtual memory!